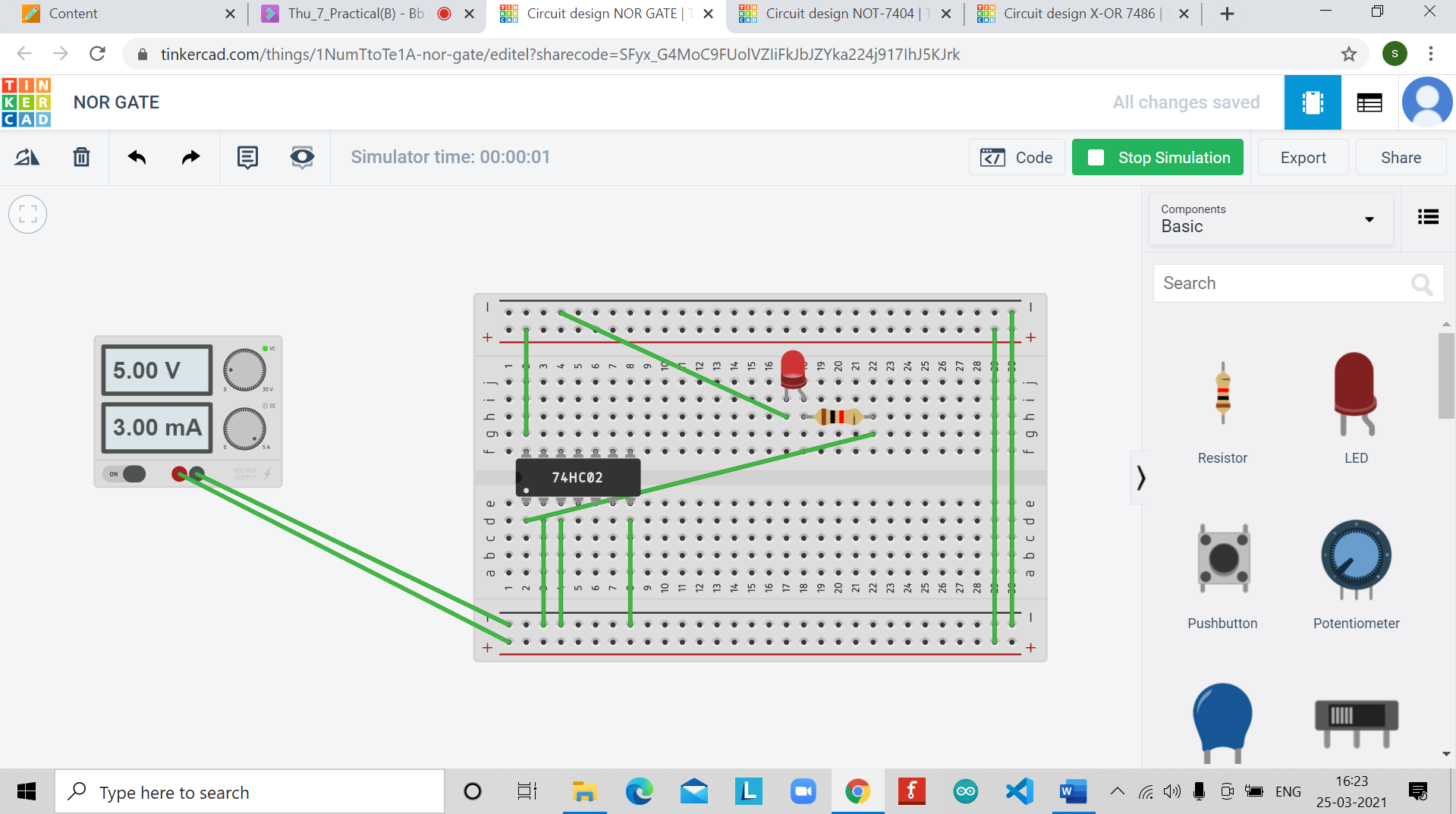
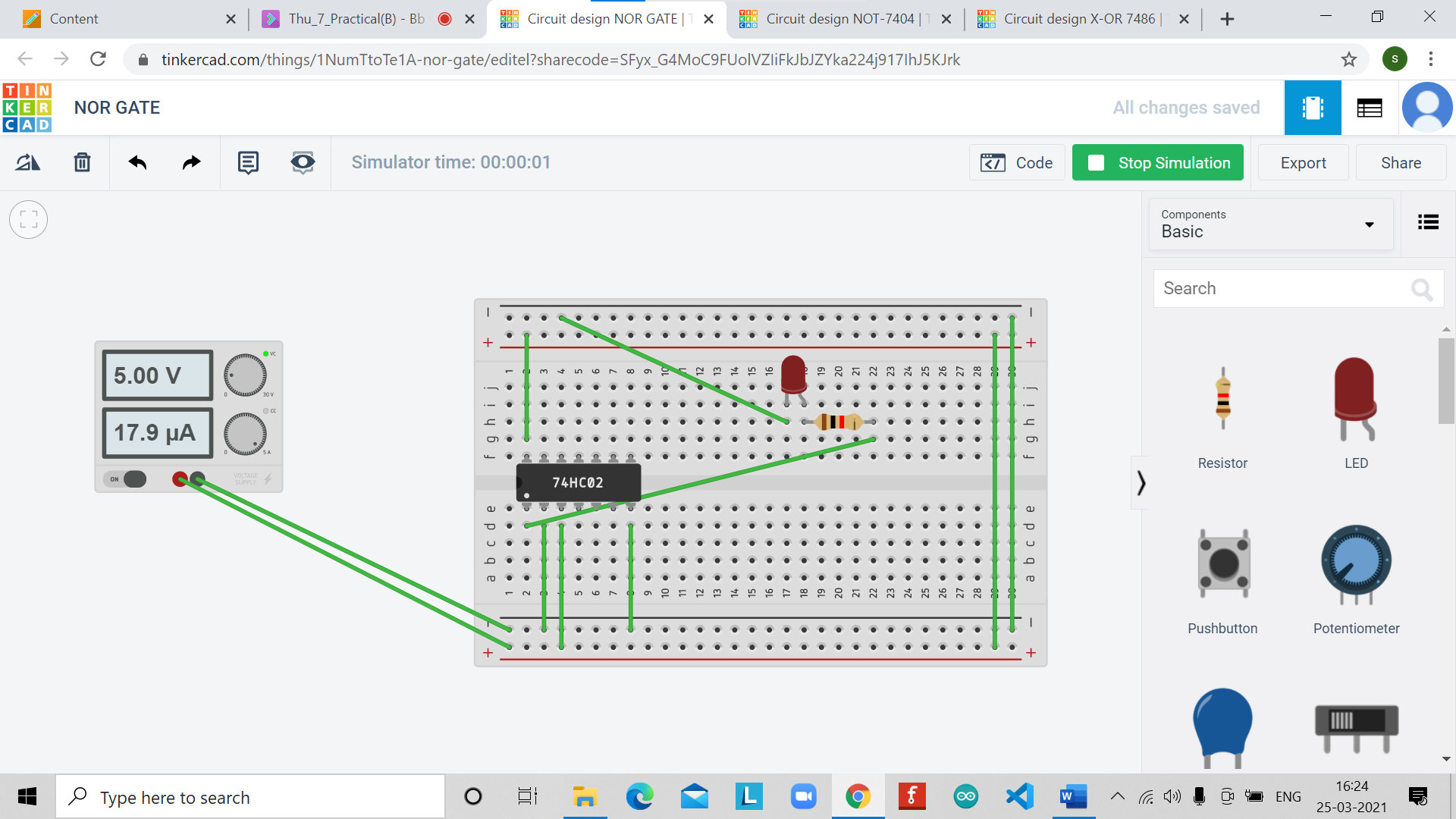
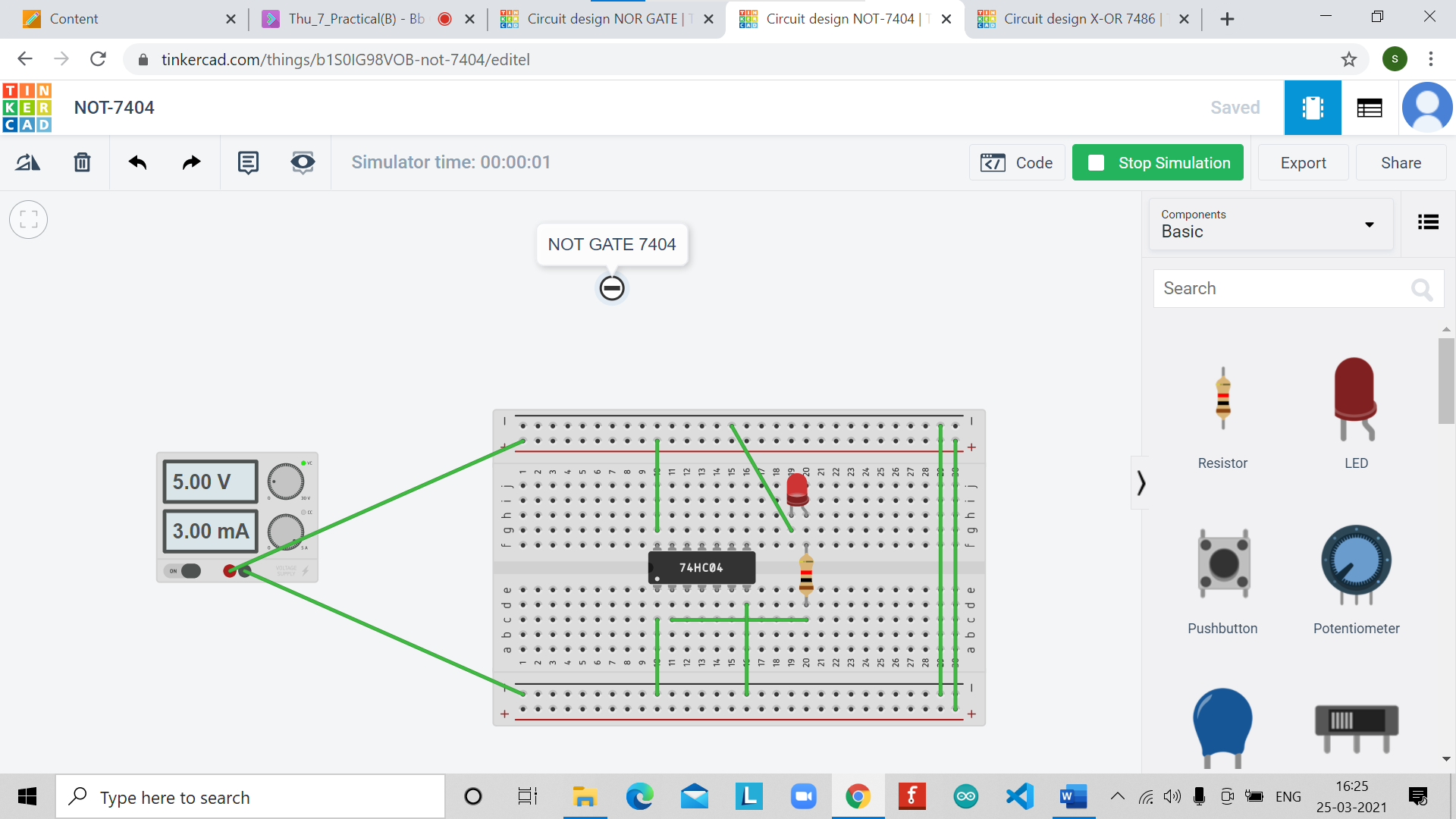


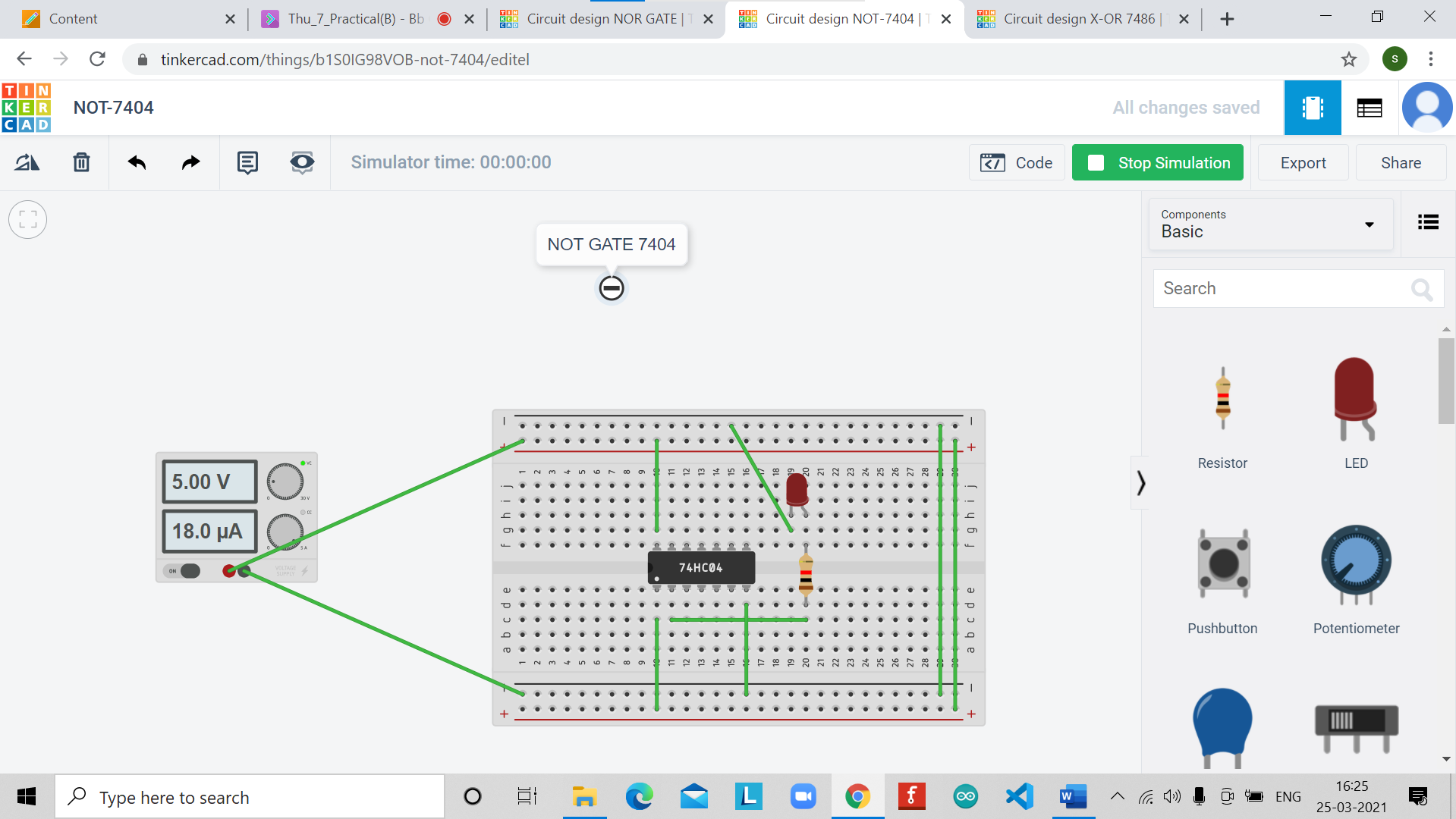
Simulation result of NOR gate





Simulation result of NOT gate





Simulation result of X-OR gate

